

PATENT APPLICATION

Sheet 1 of 3

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	10011525-1	09/924,260	
	APPLICANT		
	Evelyn Duesterwald et al.		
	FILING DATE	GROUP	
	Unknown	Unk 2122	

10011525-1 U.S. PTO.
 09/924260
 08/08/01

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
AK3	1A	6,091,897	Jul. 18, 2000	Yates et al.	395	708
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AK3	1Q	"Transparent Dynamic Optimization: The Design and Implementation of Dynamo" HP Laboratories Cambridge, HPL-1999-78, June 1999, 83 pages, by Vasanth Bala, Evelyn Duesterwald, Sanjeev Banerjia
AK3	1R	U.S. Patent Application Serial No. 09/186,945 filed Nov. 5, 1998, for "Method For Selecting Active Code Traces For Translation In A Caching Dynamic Translator" of Vasanth Bala
AK3	1S	U.S. Patent Application Serial No. 09/312,296 filed May 14, 1999, for "Low Overhead Speculative Selection Of Hot Traces In A Caching Dynamic Translator" of Vasanth Bala et al.

EXAMINER	DATE CONSIDERED
<i>Andi K. Fowler</i>	8/17/04

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PATENT APPLICATION

Sheet 2 of 3

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 10011525-1	APPLICATION NO. 09/924,260	CONFIRMATION NO.
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	1P							

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<i>AKJ</i>	1Q	U.S. Patent Application Serial No. 09/755,389 filed Jan. 5, 2001, for "A Partitioned Code Cache Organization To Exploit Program Locality" of Sanjeev Banerjia et al.
<i>AKJ</i>	1R	U.S. Patent Application Serial No. 09/755,774 filed Jan. 5, 2001, for "A Memory Disambiguation Scheme For Partially Redundant Load Removal" of Vasanth Bala et al.
<i>AKJ</i>	1S	U.S. Patent Application Serial No. 09/755,381 filed Jan. 5, 2001, "A Fast Runtime Scheme For Removing Dead Code Across Linked Fragments" of Evelyn Duesterwald et al.

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EXAMINER <i>Andi K. Zwick</i>	DATE CONSIDERED 8/17/04
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Sheet 3 of 3

FORM PTO-1449

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APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10011525-1

APPLICATION NO.

09/924,260

CONFIRMATION NO.

APPLICANT

Ev lyn Duesterwald et al.

FILING DATE

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	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>RLZ</i>	1Q	U.S. Patent Application Serial No. 09/874,170 filed Jun. 4, 2001 for "A Networked Client-Server Architecture For Transparently Transforming And Executing Applications" of Vasanth Bala et al.
	1R	
	1S	

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EXAMINER

Andre L. Bullock

DATE CONSIDERED

8/17/04